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Uniden PRO-310e Service Manual

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uniden

CB TRANSCEIVER
MODEL: PRO 310e

SERVICE INFORMATION MANUAL

UNIDEN CUSTOMER SERVICE & PARTS 9900 Westpoint Drive Indianapolis, IN 46256 (317) 842-2483

CB TRANSCEIVER

MODEL: PRO 310e

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BLOCK DIAGRAM

SCHEMATIC DIAGRAM (E12-2581)

WIRING DIAGRAM (E32-2682)

P.C.BOARD TOP VIEW

MAIN P.C.B. (E22-6670)

LED P.C.B. (E23-6671)

1. SPECIFICATIONS

CB/AM EMERGENCY TRANSCEIVER MODEL : PRO 310e (UT-311Z)

GENERAL

1. Channels : 40

2. Frequency Range : 26.965 MHz to 27.405 MHz

3. Semiconductors : 24 Transistors, 32 Diodes, 6 ICs

4. Crystal Oscillators : 1

5. Microphone : Electret Condenser Type

6. Speaker : 16 ohm, 0.4 W

7. Antenna Connector : RCA Type : For Roof Mount Antenna

5/16" Screw Type : For Portableuse

8. Jacks & Connector : DC Power Jack

9. Controls : Channel Selector UP/Down

Power Switch & Volume, Squelch

10. Illuminations : Channel Readout (LED Green),

TX Indicator (LED Red)
Battery Low (LED Red)

11. Size : W : 67mm with W : 67mm without

H : 220mm Battery Pack H : 220mm Battery Pack

D: 40.5mm D: 40.5mm

12. Weight : 335 g 260 g

13. Accessories : Magnet Base for Roof Mount Antenna,

Telescopic Antenna Element, DC Power Cord with Cigarette Lighter Plug and

Fuse Holder (Built-in 2A Fuse)

14. Grounding : Both Negative, Positive

MEASUREMENT CONDITIONS

1. Power Source : 13.8 V (DC)

2. Antenna Impedance : 50 ohm

3. Test Temperature : 25°C

4. Modulation Frequency : 1 kHz

5. Mean Signal Input Level : 1000 uV

6. Reference Audio Output Power : 0.05 W

7. Reference Modulation Percentage : 1 kHz 30 %

8. Audio Output Load : 8 ohm Resistive

9. Measuring Channel : 19

NOTE: Limit specifications are for measurements on all channels.

TRANSMITTER SECTION

	<u>ITEMS</u>	UNIT	NOMINAL	LIMIT
1.	Frequency Tolerance at 25°C (5 mimutes after Switch on)	7.	±0.002	±0.003
2.	Carrier Power at No Modulatio (High Power) (Low Power)	on W W	3.5 1.0	3 to 4 0.7 to 1.5
3.	Spurious Harmonic Emission	dB	-70	-60
4.	Microphone Sensitivity at 1 k for 50 % Mod.	cHz mV	8	15
5.	Modulation Frequency Response (1 kHz, 0 dB Reference) Lower at 300 Hz Upper at 3.0 kHz	dB dB	-6 -3	-12 -12
6.	AMC Range 50 to 100 % Mod.	dB	36	30
7.	Hum and Noise in Transmit	dB	50	40
8.	Battery Drain at No Modulation (High Power) (Low Power)	mA mA	700 450	1000 600

ITEMS		UNIT	NOMINAL	LIMIT
	igh Power)	on mA 100 mA 600		
10. Output Protec		t for 5 mim he Smith Cha		
	ity at all VSWR' c continuous ope mittting, and 1	ration of a	duty cyc	•
RECEIVER SECTION				
1. Max. Sensitivi	y	uV	0.5	1
2. Sensitivity for	10 dB S+N/N	uV	0.7	1.2
3. AGC Figure of 1 Change in Audio		10 dB dB 90	75	
4. Overall Audio Dupper Frequency Lower Frequency	y (1 kHz = 0 dB)		2500 300	2000 min 450 max
5. Adjacent Channe (Single General		dB	70	50
6. Maximum Audio (Output Power	W	0.3	0.25
7. Audio Output Po	ower at 10 % THD	W	0.25	0.2
8. Hum and Noise H	Ratio at Input 1	mV dB	30	25
9. Squelch Sensit:	vity at Thresho	old uV	0.1	1
10. Squelch Sensi	ivity at Tight	uV	1000	250 to 4000
11. Image Rejection (910 kHz)	on Ratio	dB	75	55

<u>ITEMS</u>	UNIT	NOMINAL	LIMIT
12. IF Rejection Ratio 1st and 2nd			
	dB	75	60
13. 1/2 IF Rejection Ratio	dB	70	55
14. Oscillator Dropout Volatge	V	6	9
15. Battery Drain (at DC Power Jack	+13.8 V)		
(a) Un Squelched-No Signal	mA	110	160
(b) Un Squelched-Max Power	mA	320	550
(c) Squelched	mA	100	130
16. Battery Drain (at Battery Conta	ct +12 V)	
(a) Un Squelched-No Signal	mA	100	150
(b) Un Squelched-Max Power	mA	300	500
(c) Squelched	mA	60	85
Amuen			
OTHER	unn] w		
1. Back up current drain at 12 V Su		80	120
	uA	80	120

2. ALIGNMENT PROCEDURE

1. ALIGNMENT OF P.L.L. PORTION

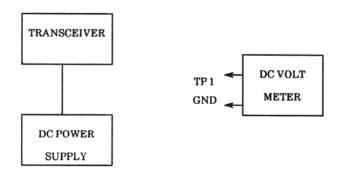
1-1. Test Equipment Required

- a. Oscilloscope (0 50 MHz)
- b. DC Power Supply (13.8 V)
- c. DC Voltmeter (10 V maximum, 100 kohu/V)

1-2. Alignment Procedure

STEP	PRESET TO	ADJUS TM ENT	REMARKS
1	TX Mode CH : 40 No Modulation	L 13	Connect DC Voltmeter to TP 2. Adjust for approx. 4.3 V on DC Voltmeter.
2	RX Mode CH : 40 No Modulation	CT 1	Same as step 1.

1-3. Test Equipment Connection



2. ALIGNMENT OF TRANSMITTER PORTION

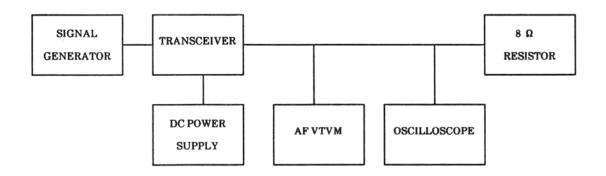
2-1. Test Equipment Required

- a. DC Power Supply (13.8 V, 2 A)
- b. Oscilloscope (0 50 MHz)
- c. Dummy Load (50 ohm, 100 ohm)
 & Attenuator
- d. AF Generator (Audio Frequency Oscillator)
- e. RF Power Meter
- f. Field Strength Meter
- g. Frequency Counter (0 50 MHz)
- h. RF SSVM (RF Voltmeter, Full scale: 1 V DC, with RF prove)

2-2. Alignment Procedure

STEP	PRESET TO	ADJUS TM ENT	R EMARKS		
1	CH: 19 No. mod. TX mode	L 11, L12	Connect prove of RF SSVM to TP 1. Adjust coils for maximum reading.		
2	Same as above.	L 7, L 10	Connect RF Power Meter to ANT. jack (J 4). Adjust coils for maximum reading.		
3	Same as step l	L 7	Adjust for 3.8 W on RF power meter.		
4	Repeat above adjustments to confirm the adjustments were made correctly.				

2-3. Test Equipment Connection



3. ALIGNMENT OF RECEIVER PORTION

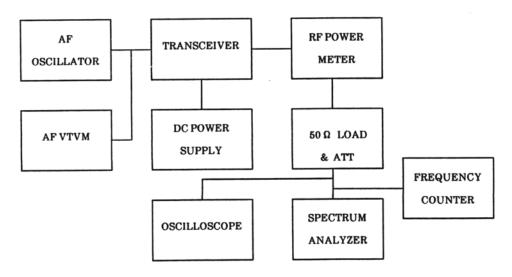
3-1. Test Equipment Required

- a. RF Signal Generator (27 MHz Band, 1000 Hz, 30 % Modulation & output Impedance 50 ohm)
- b. AF VTVM
- c. Oscilloscope (0 50 MHz)
- d. Dummy Load (8 ohm, 5 watts, resistive)
- e. DC Power Supply (13.8 V)
- f. DC Voltmeter

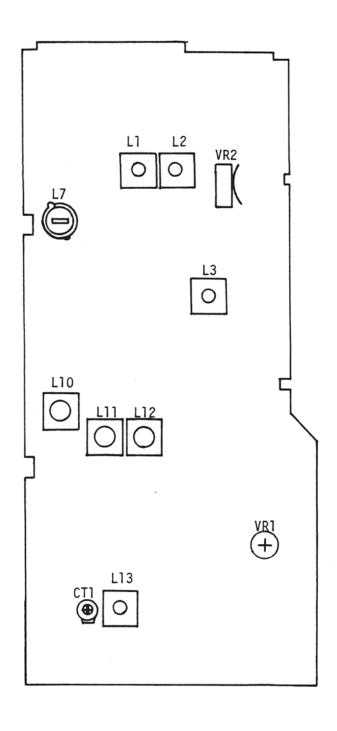
3-2. Alignment Procedure

STEP	PRESET TO	ADJUSTMENT	R EM ARKS
1	RX Mode. WOL : Max. SQL : Min. CH : 19	L 1, L 2, L 3	Connect RF SSG to ANT. Connector (J 4) and set it 27.185 MHz. Connect AF VTVM to SPK. Adjust coils for maximum reading on AF VTVM.
2	Same as above.	VR 2	Set the RF Signal Generator to 0.25 uV ouput level. Adjust VR 2 for 0.6 V on AF VTVM.
3	Same as step 1. except SQL : Max.	VR l (Squelch)	Set the RF Signal Generator to 1000 uV ouput level. Adjust VR 1 for 0.6 V on AF VTVM.

3-3. Test Equipment Connection



3. ALIGNMENT POINTS



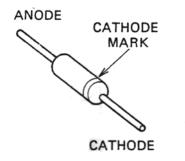
4.IC VOLTAGE CHART

IC	IC NAME	IC PIN NO	PΥ	(v)	Tx (v)
No.	20 101/12				
		/	1.0		0
		2	(.7		0
		3	6.5		0
/	1 LA 1185	4	1.6		0
'	LA 1103	5	0		0
		6	6.2		
		7	5.8		0
		8	5.5		0
		9	6.6		
	,	1	6.5		6.0
		2	1.3		0,3
		3	6.4		0.3
		4	1.3		D. 8
		5	1.3		0.7
		6	6.0		0.8
		7	6.6		0.3
2	TĐA 1220B	8	0.7		0
		9	2.0		0.2
		10	6.6		0,3
		11	0		0
		12	0		0
		13	0		0
		14	6		0
		15	6		0
		16	0		0
		/		Q Max 4.0	4.0
		2	4.0	4.0	4.0
		3	4.0	4.0	4,0
3	M 5223 L	4	0	0	0
		5	1.9	1.2	1.9
		6	1.5	1,5	6.9
		7	6.Z	0	0
		8	7.4	7.4	7,4
		1	0		6.4
		Z	0.3		12.5
		3	0.3		10.9
		4	0		0
		5	D		0
		6			2.4
		7	0.1		2,4
4	TDA 1905	8	0.1		2.4
		9	0		0
		10			0
		11	0		0
		12	0		0
		13	0		0
		14	0		0
		15	<i>D</i>		0
1	1	16	0	1	0

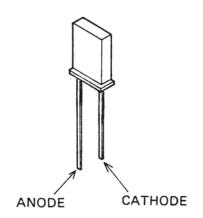
IC		1			
No.	IC NAME	IC PIN No.	ХX	(v)	⊤x (v)
740			,— <u>—</u>	5.0	5.0
		i		1	
				0.5	LJ 0.5
		2	0.		2,6
		3	0.		2, 6
				5,0	5.0
		4		0.5	L 0,5
			5.		
		- 5	5.		5,0
		6		5.0	5,0
		6		0.5	LJ 0.5
				-· 5.D	5.0
		7		-,-	5.0
			ره.٥.5ر		LJ 0.5
5	SM 5121	AA E/7/ 8		0	
フ	- 00	9	2.		2.6
	- 00	10	2.5		2.5
		[1	0		0
		12	2.3		2,3
		13	5./		5.1
	(19 cH.)	14	2,5		2,5
		15	2.7		2.7
		16		7	2.7
		17	0		0
		/8	5		0
		19		.7	2.7
		20)	0
				5.0	7 5,0
		22		5,0	
				0	LJ 0
		1	2.3	0	0
		2	5.4	0	0
		3	2.3	0	0
6	TDA 2822 M	4	0	0	0
ð	100 20 221	5	0.6	0	0
		6	0	0	0
		7	0	0	0
		8	0.6	0	0

5. SEMICONDUCTOR LEAD IDENTIFICATION

DIODE

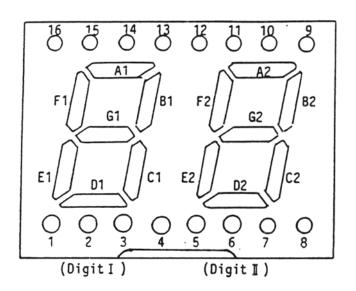


1N60P	HZ5C-1
1S2339G	HZ3B3
MC-301	HZ-5C3
1N4003	HZ-6C1
1S1555	HZ-9A1



RT-242

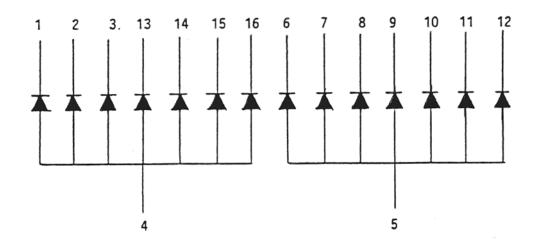
DLS1585



UG-203

PIN NO.

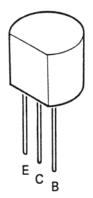
1.	C1	9.	G2
2.	E1	10.	A2
3.	D1	11.	F2
4.	Common Digit I	12.	B2
5.	Common Digit I	13.	B1
6.	D2	14.	F1
7.	E2	15.	A1
8.	C2	16.	G1



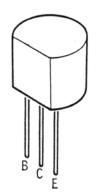
TRANSISTOR

NOTE: B: Base

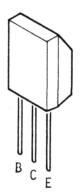
C: Collector E: Emitter



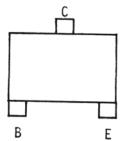
2SC941 2SC3242A-E



2SB525 2SC2086



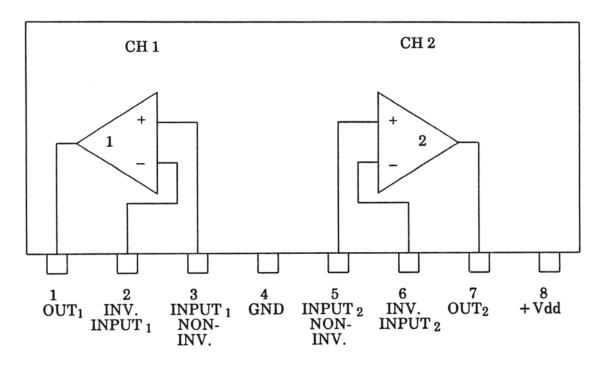
2SB753 2SC2166

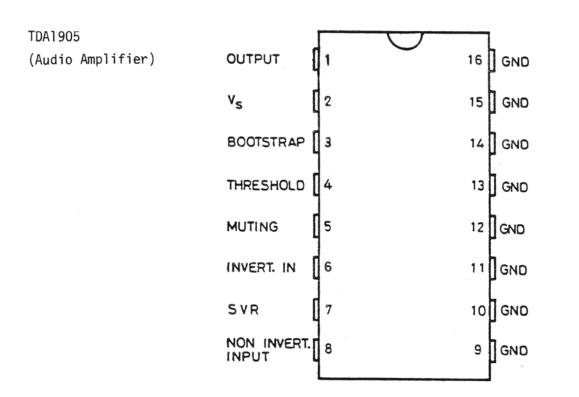


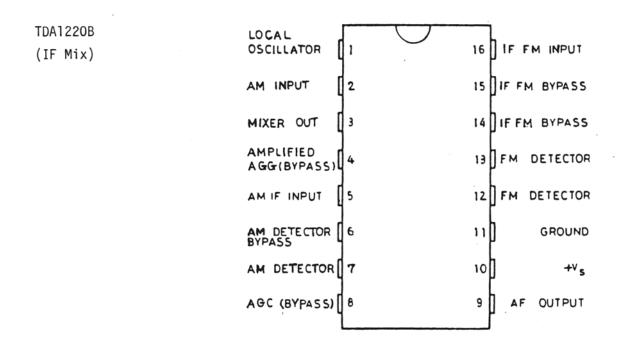
2SA1179-M6 2SD3242A-E 2SC2812-L5

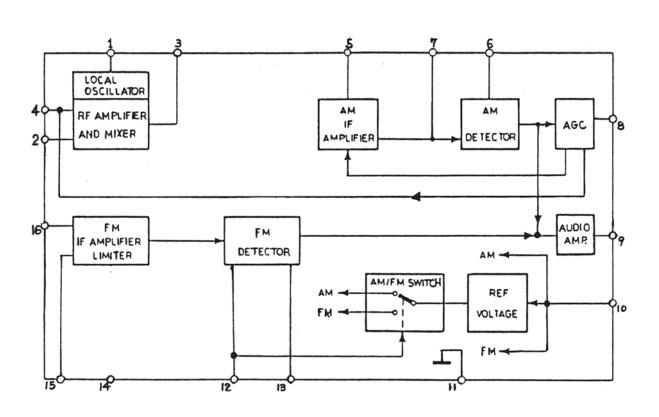
2SC2814-F5

M5223L (Squelch and AGC Amplifier)

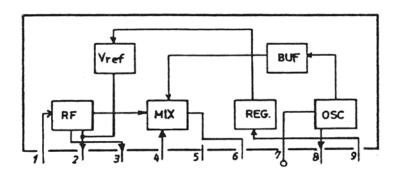




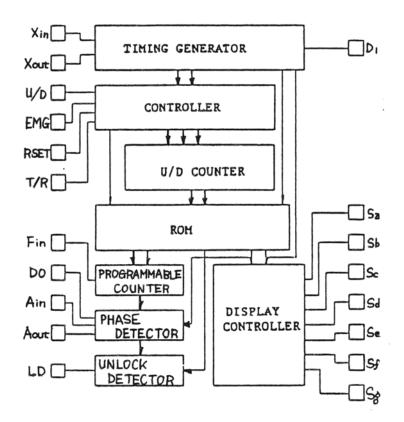




LA-1185



SM5121-00



Sa		1	\bigcup	22	þ	Dı
Sb	Ц	2		2 1	þ	RSFT
Sc		3		20	þ	U/D
Sd		4		19	þ	EMG
Se		5		18	þ	T/R
Sf		6		17	þ	LD
Sg		7		16	þ	D_0
v_{ss}		8		15	þ	Àτ
Xout		9		1 4	þ	A ₀
Xin		10		1 3	þ	ν_{dd}
v_{SB}		1 1		12	þ	\mathbf{F}_{1N}

Pin No.	PIN NAME	Function
1 through 7	Sa through Sg	Display Segment Driver
.8	Vss	Ground
9,10	Xout ,Xin	Crystal Oscillation Circuit; 10.24MHz
11	V sB	Channel Memory Back Up; Vss level
1 2	Fin	Input of Programmable Divider
13	V dd	Power Supply Pin; +5.8Y
14	Ao	Amplifier Output for LPF
15	Ai	Amplifier Input for LPF
16	Do	Output of Phase Comparator
17	LD	PLL Lock Detector, "L";Unlock
18	T/R	"H";Transmitter, "L";Receiver
19	EMG	Emergency Channel Call, "H";ON
2 0	מ/ט	Channel Up/Down Switch
		"H";Up, "L";Down, "Open";No Operation
2 1	Rsft	Receiving Code Shift, "H";N+1
2 2	D1	Display Digit Controll

